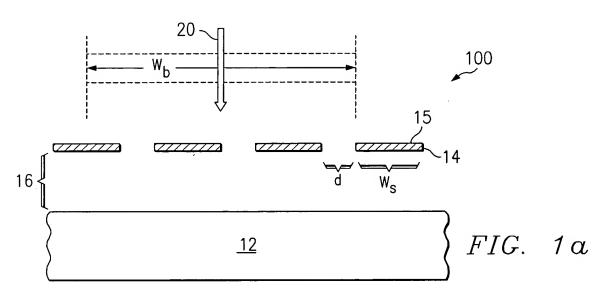
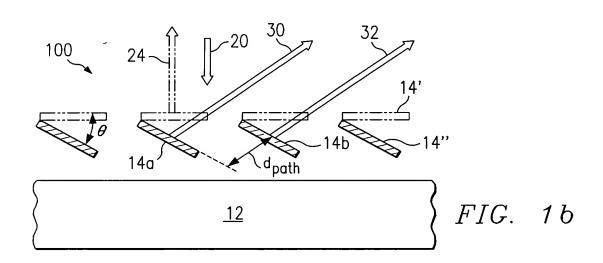
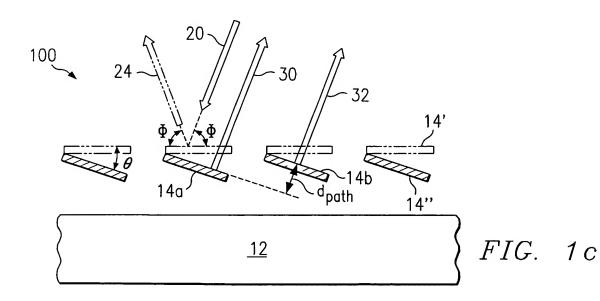
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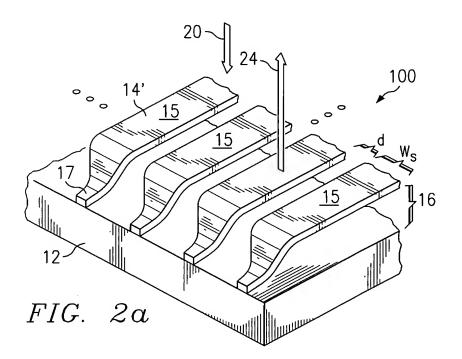
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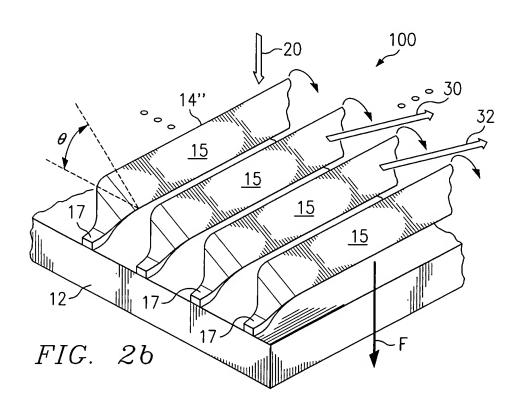




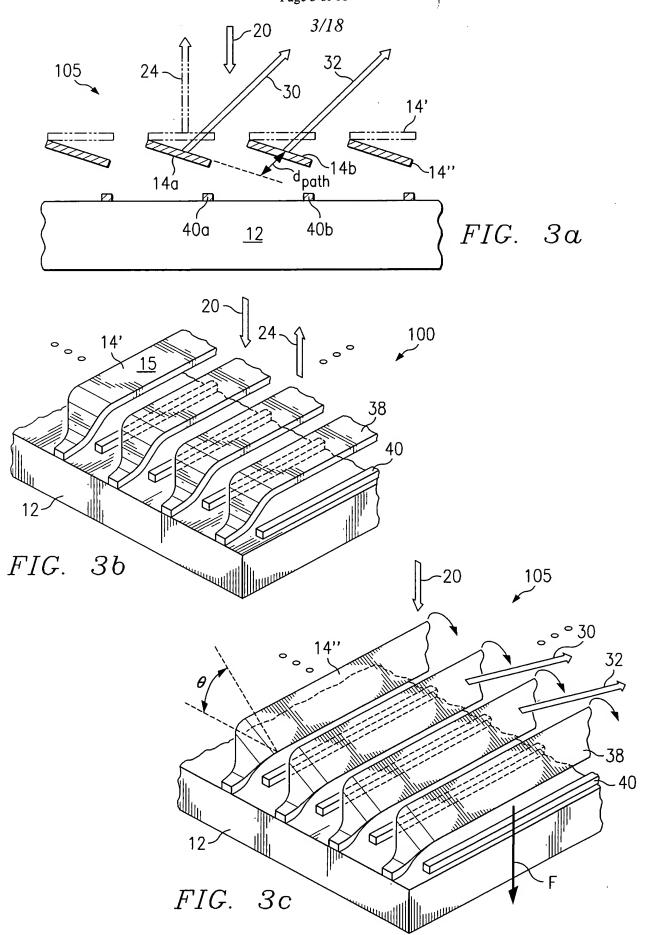


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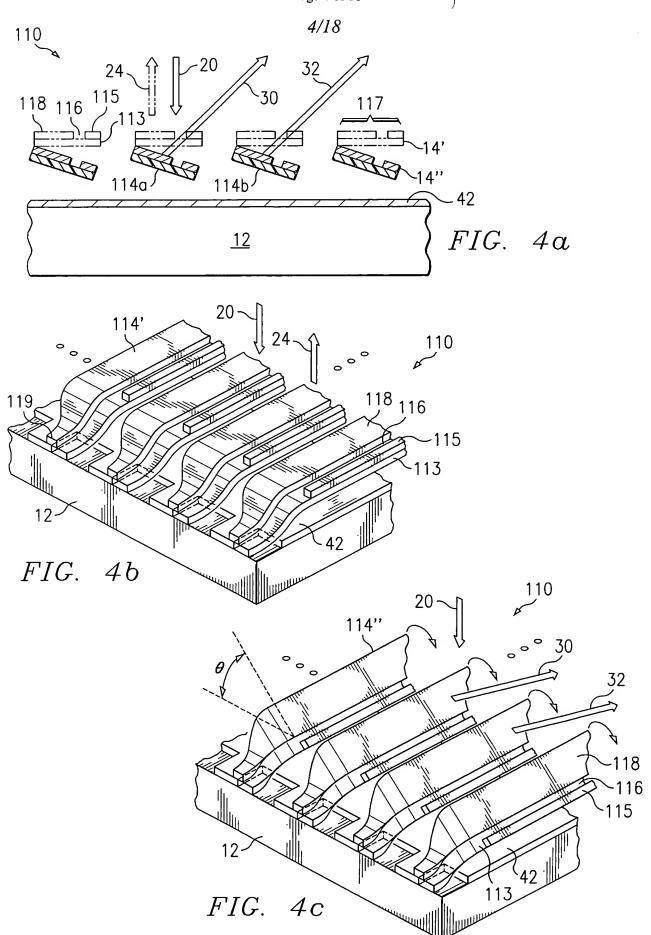




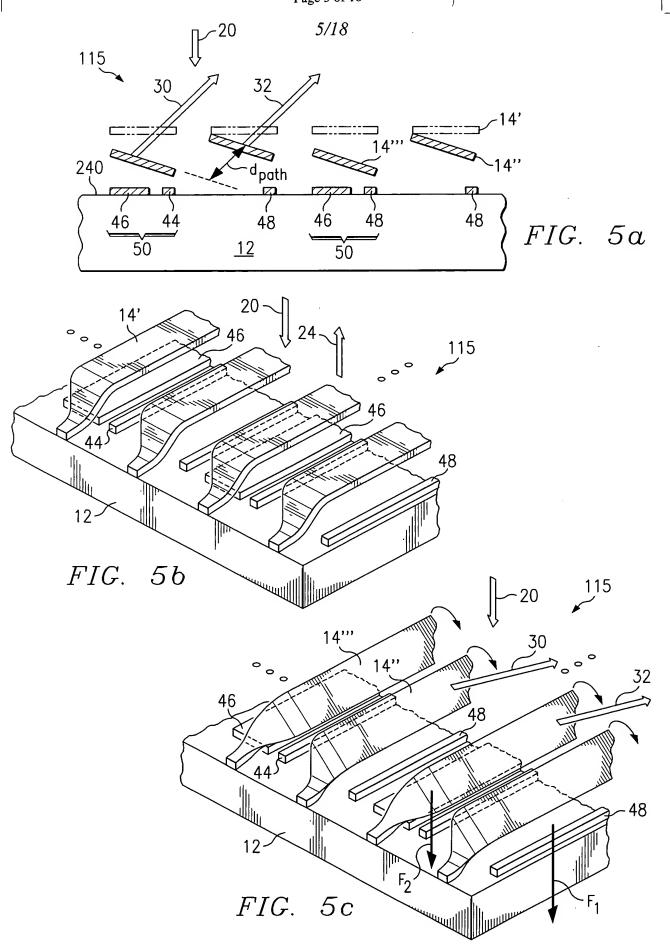
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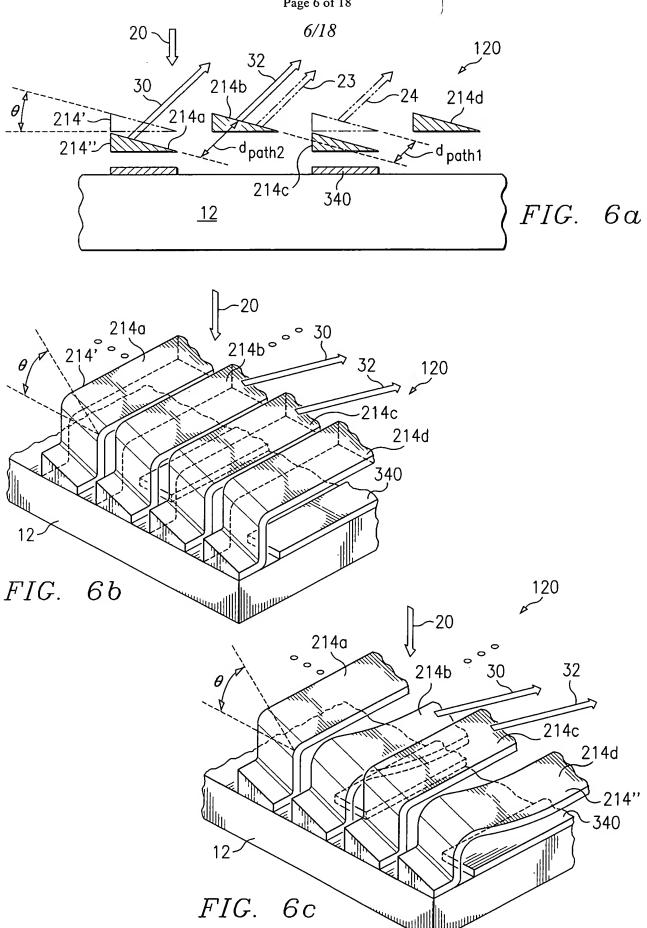
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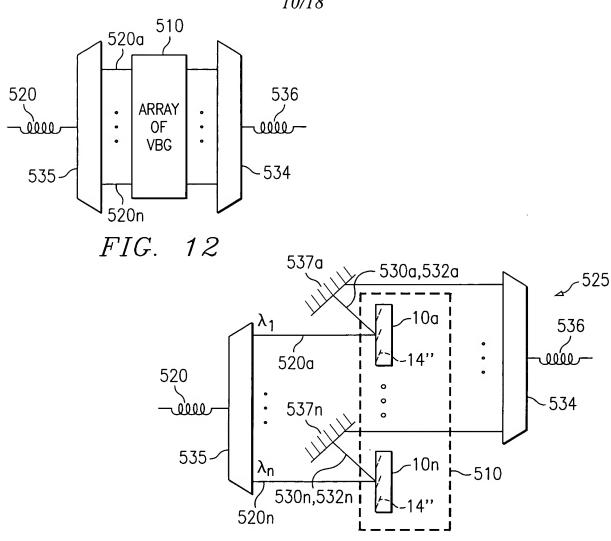
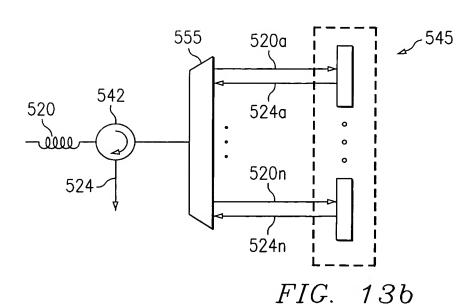
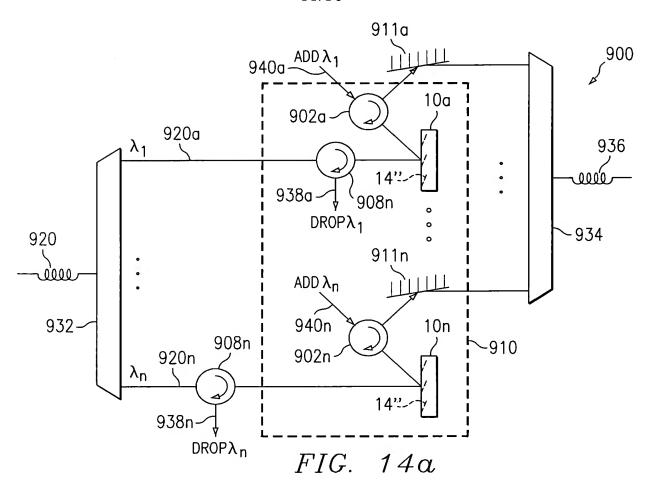


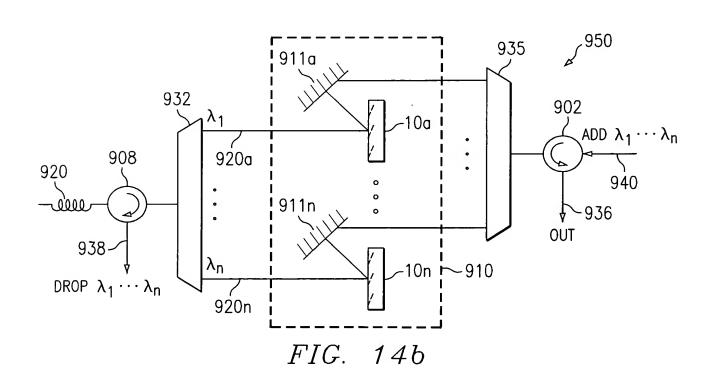
FIG.

 13α



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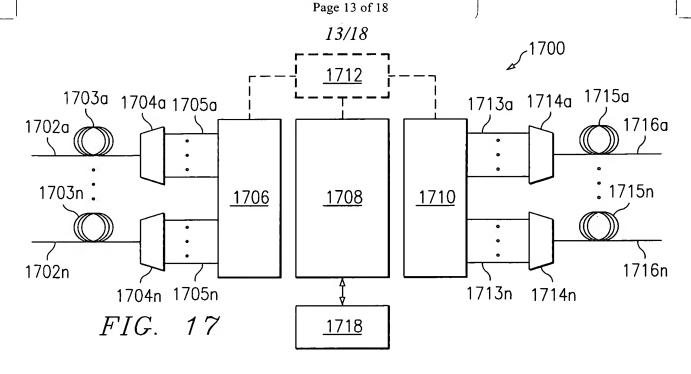


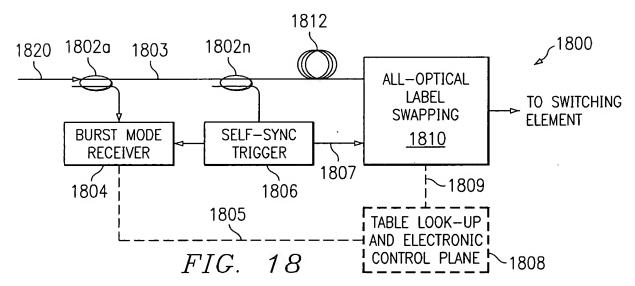


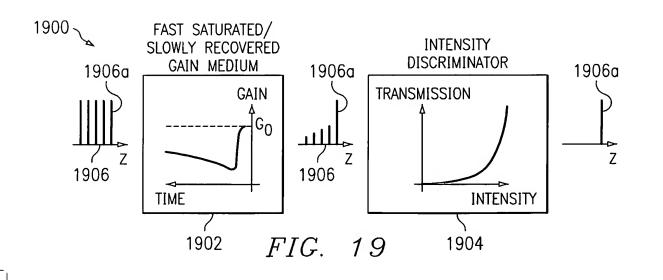
Filing Date: November 25, 2003 Page 12 of 18 12/18 1028 1012 1024 1020 1014 λ₁ 1 2 1016 1020 FIG. 15 DEMUX **ELECTRONIC PROCESSOR** N -1030 1026 1022-1016 **ARRAY** DEMUX OUT OF **VBG** 1032--1036 400 FIG. 16 **START** 410 RECEIVE FIRST OPTICAL SIGNAL 420 REFLECT FIRST OPTICAL SIGNAL IN FIRST DIRECTION WHILE BLAZED GRATING RESIDES IN FIRST POSITION 430 MOVE BLAZED GRATING TO SECOND POSITION DIFFRACT MAJORITY OF FIRST OPTICAL SIGNAL IN SECOND 440 DIRECTION WHILE BLAZED GRATING RESIDES IN SECOND POSITION RECEIVE SECOND OPTICAL SIGNAL - 450 REFLECT SECOND OPTICAL SIGNAL IN SECOND DIRECTION - 460 WHILE BLAZED GRATING RESIDES IN FIRST POSITION DIFFRACT MAJORITY OF SECOND OPTICAL SIGNAL IN FIRST DIRECTION WHILE BLAZED GRATING RESIDES IN SECOND POSITION

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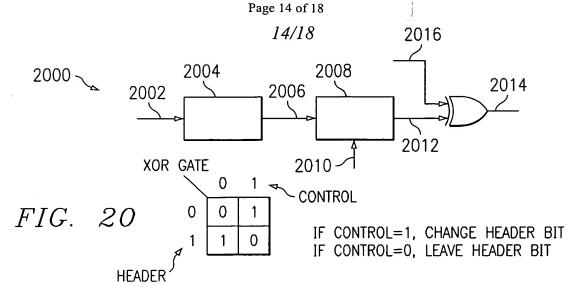
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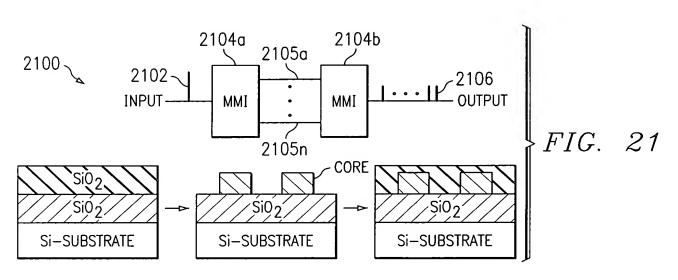






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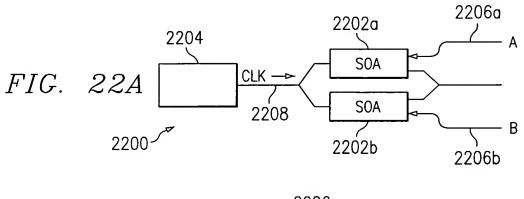
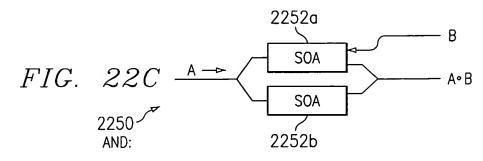
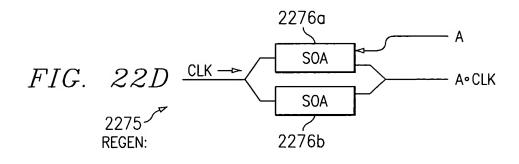
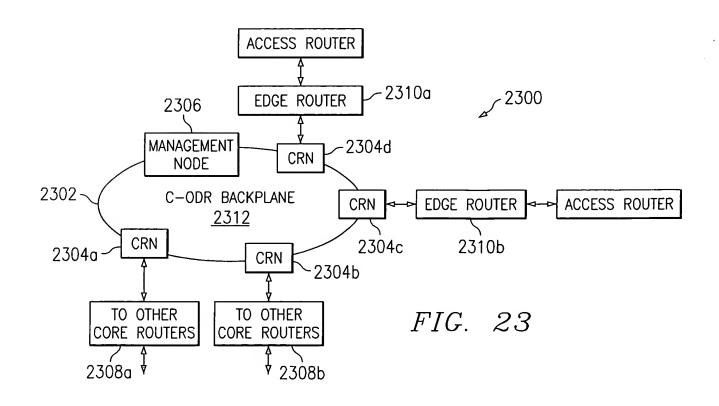


FIG.
$$22B$$
 CLK SOA \overline{A} SOA \overline{A} CLK INVERTER: 2226b

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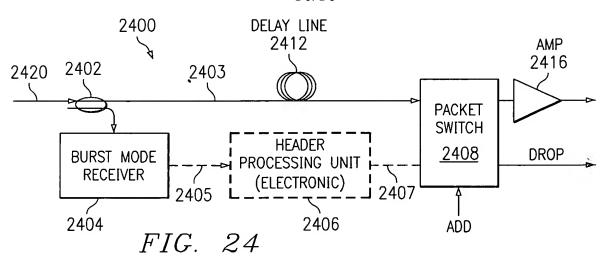


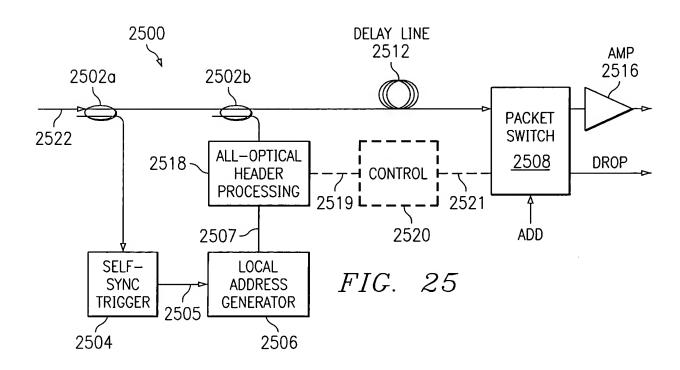
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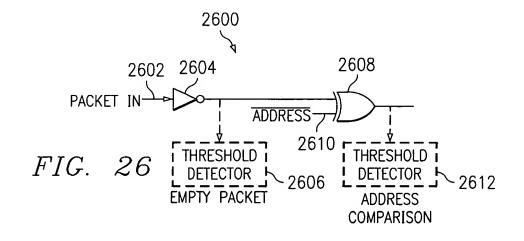
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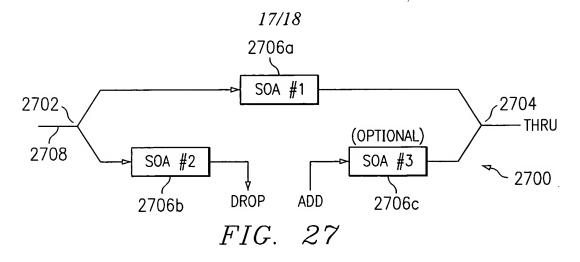
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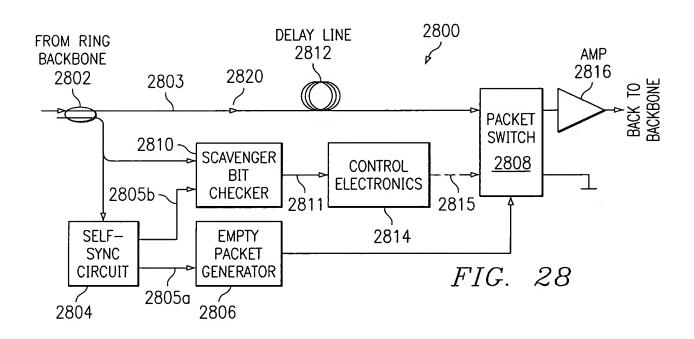


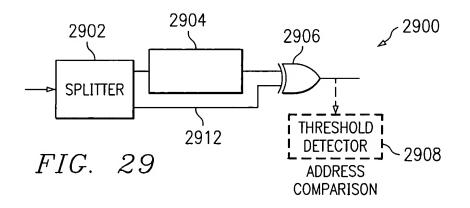




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